

# ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	BRANCH INSTRUCTION FOR PROCESSOR ARCHITECTURE						
Application Number :	10/069229 						
Confirmation Number:	1429						
First Named Applicant:	Gilbert Wolrich						
Attorney Docket Number:	10559-305US1						
Art Unit:							
Examiner:							
Search string:	( 5937187 or 5938736 or 5940612 or 5940866 or 5946487 or 5948081 or 5951679 or 5958031 or 5961628 or 5970013 or 5978838 or 5983274 or 5996068 or 6002881 or 6009515 or 6012151 or 6014729 or 6023742 or 6029228 or 6058168 or 6058465 or 6067585 or 6070231 or 6072781 or 6073215 or 6076158 or 6079008 or 6079014 or 6085215 or 6085294 or 6092127 or 6092158 or 6092175 or 6112016 or 6115811 or 6134665 or 6141689 or 6141765 or 6144669 or 6145054 or 6157955 or 6160562 or 6182177 or 6195676 or 6199133 or 6201807 or 6212542 or 6212611 or 6216220 or 6223207 ).pn						
<b>US Patent Documents</b>							
<b>Note: Applicant is not required to submit a paper copy of cited US Patent Documents</b>							
init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
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2	5938736	1999-08-17	Muller et al.				
3	5940612	1999-08-17	Brady et al.				
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10	5970013	1999-10-19	Fischer et al.				
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15	6009515	1999-12-28	Steele Jr.				
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	49	6216220	2001-04-10	Hwang			
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### Signature

Examiner Name	Date